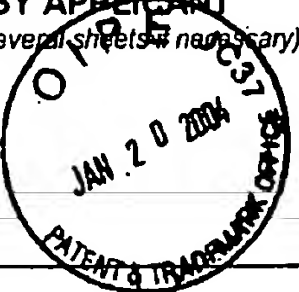


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		APPLICANT Olszewski et al.			
		FILING DATE May 31, 2001		GROUP ART UNIT 2127	
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
LV	AA	EP 0 827 071 A2	Mar. 4, 1998	Europe	G06F 9/38 X
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)					
LV	AB	Lo et al., "Software-Directed Register Deallocation for Simultaneous Multithreaded Processors", IEEE Transactions on Parallel and Distributed Systems, Vol. 10, No. 9, Sept. 1999, pp. 922-933.			
LV	AC	Zilles et al., "The Use of Multithreading for Exception Handling", Proceedings of the 32nd Annual ACM/IEEE International Symposium on Microarchitecture. Haifa, Israel, Nov. 1999, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA, IEEE Comp. Soc. US, Nov. 1999, pp. 219-229.			
LV	AD	Wallace et al., "Threaded Multiple Path Execution", Computer Architecture, 1998, pp. 238-249.			
LV	AE	Farcy et al., "Improving Single-Process Performance with Multithreaded Processors", Proceedings of the 1996 International Conference on Supercomputing. Philadelphia, May 1996, pp. 350-357.			
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE
DATE CONSIDERED 8/9/05		EXAMINER Lilian V.			
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